

Amendments to the Specification:

Please replace paragraph [0026] with the following amended paragraph, with insertions underlined (e.g., insertion), and deletions struck-through or in double brackets (e.g., ~~deletion~~ or [[deletion]]):

[0026] The original register set 210 includes N original registers r_1 to r_N . These original registers r_1 to r_N are the architectural registers of the processor [[100]] 110 or a source processor that the original code runs on. The original registers r_1 to r_N may be a register file used by the instruction set architecture (ISA) for the underlying processor. In one embodiment, the registers r_1 to r_N are the floating point (FP) registers used in the IA-32 or IA-64 processors. The original register set 210 represents the source registers.

Please replace paragraph [0045] with the following amended paragraph, with insertions underlined (e.g., insertion), and deletions struck-through or in double brackets (e.g., ~~deletion~~ or [[deletion]]):

[0045] Next, the process 300 determines if the last use of the canonical register C_i occur before the last definition of the original register r_i (Block 330). If so, the process 300 applies a rollback to the original register r_i (Block 340) and is then terminated. Otherwise, the process 300 applies a [[rollback]] recovery to original register r_i (Block 350) and is then terminated.